

Notice of References Cited	Application/Control No. 10/707,388	Applicant(s)/Patent Under Reexamination DIVAKARUNI ET AL.	
	Examiner Steven J. Fulk	Art Unit 2891	Page 1 of 1

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NON-PATENT DOCUMENTS

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*	U	Wolf, "Silicon Processing for the VLSI Era: Process Integration," Vol. II, 1990, pp. 146, 176, 193.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.